

## PATENT

*faxed to Examiner David Eng at*  
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 the Assistant Commissioner for Patents, Washington, D.C. 20231, on February 5, 1998

Date: 4/5/98

By:

Patricia K. Parry / Patricia K. Parry

Fax #: 703-308-5356

Time: 9:00 AM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Charles H. Moore et al.

Serial No. 08/484,935

Filed: June 7, 1995

For: HIGH PERFORMANCE  
 MICROPROCESSOR USING  
 INSTRUCTION GROUPS

)Examiner: D. Eng

)Group Art Unit: 2315

)Palo Alto, CA

#14/7  
 1/12/98  
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SUPPLEMENTAL AMENDMENT

Commissioner of Patents  
 and Trademarks  
 Washington, D.C. 20231

Sir:

In response to a telephone interview with the Examiner on January 29 and  
 February 2 1998, please amend the above application as follows:

In the Claims:

Rewrite claims 71, 74, 82, 86, 87, 91, 94 and 97 as follows:

- 71 (Four Times Amended). A microprocessor system comprising:
- a central processing unit;
  - memory;
  - a bus connecting said central processing unit to said memory;